

S/N 10/692,611



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Mohan Chandra et al.

Examiner: G. Nagesh Rao

Serial No.: 10/692,611

Group Art Unit: 1722

Filed: October 24, 2003

Docket No.: 884.B40US1

Title: DEVICE AND METHOD FOR  
MULTICRYSTALLINE SILICON WAFERS

Assignee: Intel Corporation

Customer Number: 21186

AMENDMENT AND RESPONSE UNDER 37 CFR § 1.111

MS Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

This responds to the Office Action mailed on March 2, 2006. Please amend the above-identified patent application as follows.

This response is accompanied by a Petition, as well as the appropriate fee, to obtain a two-month extension of the period for responding to the Office Action, thereby moving the deadline for response from June 2, 2006 to August 2, 2006.